

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SILICON LABORATORIES, INC.
Petitioner

v.

CRESTA TECHNOLOGY CORPORATION
Patent Owner

CASE: IPR2015-00626
Patent No. 7,265,792

Title: Television Receiver for Digital and Analog Television Signals

PETITIONER'S UPDATED EXHIBIT LIST
AS OF JUNE 1, 2016

EXHIBIT LIST

- Exhibit 1001 U.S. Patent No. 7,265,792, titled “Television Receiver for Digital and Analog Signals,” to Favrat et al. (“the ‘792 patent”).
- Exhibit 1002 Return of Service of for the Complaint in *Cresta Technology Corporation v. Silicon Laboratories, Inc. et al.*, Case No. 1:14-cv-00078-RGA before the United States District Court for the District of Delaware (January 28, 2014) (“Return of Service”).
- Exhibit 1003 Complaint in ITC Investigation No. 337-TA-910 (January 28, 2014) (“ITC Complaint”).
- Exhibit 1004 European Patent Application No. EP 0696854, titled “Broadcast Receiver Adapted for Analog and Digital Signals,” assigned to Thomson Multimedia S.A. (“Thomson”).
- Exhibit 1005 Clay Olmstead & Mike Petrowski of Harris Semiconductor, *A Digital Tuner for Wideband Receivers*, DSP Applications Magazine, Sep. 1992 (“Harris”).
- Exhibit 1006 Al Lovrich & Ray Simar, Jr., “Implementation of FIR/IIR Filters with the TMS3201/TMS32020,” Application Report: SPRA003A (Texas Instruments 1989) (“TI App Note”).
- Exhibit 1007 U.S. Patent Application No. 2004/0061804, titled “Broadband Receiver Having a Multistandard Channel Filter,” to Favrat et al. (“the ‘585 application”).
- Exhibit 1008 U.S. Patent 5,381,357, titled “Complex Adaptive FIR Filter,” to Wedgwood et al. (“Grumman”).
- Exhibit 1009 Declaration of Douglas Holberg (“Holberg Dec.”).
- Exhibit 1010 Excerpts of the prosecution file history of the ’792 patent.

- Exhibit 1011 U.S. Patent 6,377,316, titled “Tuner with Switched Analog and Digital Modulators,” to Mycynek et al. (“Zenith”).
- Exhibit 1012 U.S. Patent 5,999,567, titled “Method for Recovering a Source Signal from a Composite Signal and Apparatus Therefor,” to Torkkola. (“Torkkola”).
- Exhibit 1013 U.S. Patent 6,263,222, titled “Signal Processing Apparatus,” to Diab et al. (“Diab”).
- Exhibit 1014 Order No. 50 in ITC Investigation No. 337-TA-910, “Initial Determination Granting Cresta Technology Corporation’s Corrected Motion to Partially Terminate Investigation as to U.S. Patent No. 7,251,466 and Certain Claims of U.S. Patent Nos. 7,075,585 and 7,265,792” (November 12, 2014).
- Exhibit 1015 U.S. Patent 6,725,463, titled “Dual Mode Tuner for Co-Existing Digital and Analog Television Signals,” to Birleson. (“Birleson”).
- Exhibit 1016 Advanced Television Systems Committee, “ATSC Digital Television Standard” (September 16, 1995).
- Exhibit 1017 U.S. Patent 5,796,442, titled “Multi-Format Television Receiver,” to Gove et al. (“Gove”).
- Exhibit 1018 U.S. Patent Application No. 2002/0057366, titled “Decoder Device and Receiver Using the Same,” to Oku et al. (“Oku”).
- Exhibit 1019 U.S. Patent No. 4,872,054, titled “Video Interface for Capturing an Incoming Video Signal and Reformatting the Video Signal,” to Gray et al. (“Gray”).
- Exhibit 1020 U.S. Patent No. 6,600,747, titled “Video Monitor Multiplexing Circuit,” to Sauber (“Sauber”).
- Exhibit 1021 U.S. Patent No. 6,804,497, titled “Partitioned Radio-Frequency Apparatus and Associated Methods,” to Kerth et al. (“Kerth”).

- Exhibit 1022 European Patent Application No. EP 94300649.4, titled “Digital Filtering, Data Rate Conversion and Modem Design,” filed January 28, 1994, to Terrell (“Terrell”).
- Exhibit 1023 U.S. Patent No. 6,124,898, titled “Digital Television Receiver with Equalization Performed on Digital Intermediate-Frequency Signals,” to Patel et al. (“Patel”).
- Exhibit 1024 U.S. Patent No. 6,411,136, titled “Differential Line Driver,” to Nianxiong et al. (“Ericsson”).
- Exhibit 1025 U.S. Patent No. 7,394,857, titled “Flexible Versatile Low-Cost Wireline Transmit Driver,” to Maulik et al. (“Maulik”).
- Exhibit 1026 U.S. Patent No. 4,595,910, titled “Digital-to-Analog Converter Useful in a Television Receiver,” to Wine (“Wine”).
- Exhibit 1027 U.S. Patent No. 4,292,597, titled “Circuit for Converting Single-Ended Input Signals to a Pair of Differential Output Signals,” to Niimura et al. (“Niimura”).
- Exhibit 1028 The Authoritative Dictionary of IEEE Standards Terms, pp. 87, 912, IEEE Press (2000).
- Exhibit 1029 U.S. Patent No. 6,332,028, titled “Dual-Processing Interference Cancelling System and Method,” to Marash (“Marash”).
- Exhibit 1030 U.S. Patent No. 6,142,942, titled “Ultrasound Imaging System and Method Employing an Adaptive Filter,” to Clark (“Clark”).
- Exhibit 1031 U.S. Patent No. 6,400,598, titled “Programmable Circuit Devices with Low Voltage Differential Signaling Capabilities,” to Nguyen et al. (“Nguyen”).

- Exhibit 1031 U.S. Patent No. 6,400,598, titled “Programmable Circuit Devices with Low Voltage Differential Signaling Capabilities,” to Nguyen et al. (“Nguyen”).
- Exhibit 1032 Microsoft Computer Dictionary, pp. 269, 270, Microsoft Press (5th ed. 2002).
- Exhibit 1033 James Karki, “Fully-Differential Amplifiers,” Application Report: SLOA054D (Texas Instruments 2002) (“Karki”).
- Exhibit 1034 Stephen Kempainen, “Low-Voltage Differential Signaling (LVDS),” Application Note 1382-6 (Agilent Technologies May 20, 2002) (“Kempainen”).
- Exhibit 1035 “High-Bandwidth LVDS Support in APEX Devices,” M-SSAPEXLVDS-02 (Altera Corporation 2000) (“Altera”).
- Exhibit 1036 “LVDS Owner’s Manual: Low-Voltage Differential Signaling,” 3d Ed., 550062-003 (National Semiconductor January 2004) (“National Semiconductor”).
- Exhibit 1037 “Product Specification for TDA10021HT DVB-C channel receiver,” (Philips Semiconductors, October 1, 2001) (“TDA10021HT”).
- Exhibit 1038 “TDA10046HT Single-Chip DVB-T channel decoder,” Document order No. 9397 750 0922 (Philips Semiconductors, May 2002) (“TDA10046HT”).
- Exhibit 1039 “Pre-Trial Brief of the Office of Unfair Import Investigations” in ITC Investigation No. 337-TA-910 (Redacted) (November 10, 2014).
- Exhibit 1040 Excerpts of “Hearing, Vol. 5” in ITC Investigation No. 337-TA-910 (December 5, 2014) (cross-examination of Dr. Caloyannides).

- Exhibit 1053 U.S. Pat. No. 6,369,857, titled “Receiver for Analgo and Digital Television Signals,” to Balaban et al.” (“Balaban”).
- Exhibit 1054 Excerpt of “IEEE Standard Dictionary Of Electrical And Electronics Terms,” IEEE Std 100-1996 (Sixth ed. 1996).
- Exhibit 1055 Satellite Receiver User’s Guide, General Electric GRD33G2A, published 1996 by Thomson Consumer Electronics, Inc. (“GE”).
- Exhibit 1059 K.G. Jackson & G.B. Townsend (eds.), “TV & Video Engineer’s Reference Book,” Butterworth-Heinemann (1991) (excerpts) (“Jackson”).
- Exhibit 1060 Deposition Transcript of Ion E. Opris, Ph.D. (Jan. 13, 2016).
- Exhibit 1061 Declaration of Douglas Holberg in Support of Petitioner’s Reply to Patent Owner’s Responses (“Holberg Rep.”).
- Exhibit 1062 Final Written Decision, IPR2014-00809, Paper No. 56 (PTAB Oct. 21, 2015).
- Exhibit 1063 Supplemental Declaration of Douglas Holberg, Ph.D., in Support of Petitioner’s Reply (Feb. 12, 2016) (“Holberg Supp.”).
- Exhibit 1064 Pleadings from Bankruptcy Proceeding 16-50808 (N.D. Cal.) re: Terminating and Annulling the Automatic Stay Re: Certain Pre-Petition Litigations.
- Exhibit 1065 Petitioner’s Demonstrative Exhibits.

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Dated: June 1, 2016

Respectfully submitted,

/s/ *Peter J. Ayers*

Peter J. Ayers

Registration No. 38,374

LEE & HAYES, PLLC

11501 Alterra Parkway, Ste. 450

Austin, TX 78758

Phone: (512) 605-0252

Facsimile: (512) 605-0269

Attorneys for Petitioner

Silicon Laboratories, Inc.

CERTIFICATE OF SERVICE

I hereby certify that on this 1st day of June 2016, the foregoing **PETITIONER'S UPDATED EXHIBIT LIST AS OF June 1, 2016, along with PETITIONER'S NEWLY ADDED EXHIBIT NUMBERED 1065** were served upon Patent Owner's attorney of record by emailing to the following addresses:

Michael R. Fleming
Lead Counsel for Patent Owner
Benjamin Haber
Backup Counsel for Patent Owner
Irell & Manella LLP
1800 Avenue of the Stars
Suite 900
Los Angeles, CA 90067
CrestaIPRs@irell.com

Mihai Murgulescu
Cresta Technology Corporation
3900 Freedom Circle, Suite 201
Santa Clara, CA 95054
mihaihm2000@gmail.com

/s/ Peter J. Ayers

Peter J. Ayers
Registration No. 38,374