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Filed on behalf of: Samsung Electronics Company, Ltd.

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UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

NVIDIA CORPORATION
Petitioner

v.

SAMSUNG ELECTRONICS COMPANY, LTD.
Patent Owner

Case IPR2015-01327
Patent No. 6,287,902

PATENT OWNER'S CURRENT LIST OF EXHIBITS

LIST OF EXHIBITS

| Exhibit No. | Description | Previously Submitted |
|--------------------|---|-----------------------------|
| 2001 | Memorandum Opinion in <i>Samsung Electronics Co., Ltd. et al. v. NVIDIA Corp. et al.</i> , 3:14-cv-00757-REP (E.D. Va. July 30, 2015) | X |
| 2002 | Order in <i>Samsung Electronics Co., Ltd. et al. v. NVIDIA Corp. et al.</i> , 3:14-cv-00757-REP (E.D. Va. July 30, 2015) | X |
| 2003 | MOSIS Scalable CMOS Design Rules (revision 7) | |
| 2004 | Annotated Portions of <i>Lee</i> (Ex. 1107) from Deposition of Dr. Jack Lee (Feb. 25-26, 2016) | |
| 2005 | Transcript of Deposition of Dr. Jack Lee (February 25-26, 2016) | |
| 2006 | Declaration of Dr. Richard Fair (March 10, 2016) | |
| 2007 | M. Koyanagi et al., IEEE SSCS News, pp. 37–41, Winter 2008 | |
| 2008 | U.S. Patent No. 5,142, 438 | |
| 2009 | G. Fonstad, <i>Microelectronic Devices and Circuits</i> , McGraw Hill, 1994, pp. 241-242 | |
| 2010 | Wolf and R.N. Tauber, <i>Silicon Processing</i> , Vol. 2, Lattice Press, 1990, pp. 17-28 | |

Dated: March 10, 2016

Respectfully submitted,

/Naveen Modi/

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CERTIFICATE OF SERVICE

I hereby certify that on this 10th day of March 2016, a copy of Patent Owner's Current List of Exhibits and the listed exhibits were served by electronic means, as agreed by the parties, upon Counsel for Petitioner at the following addresses of record:

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