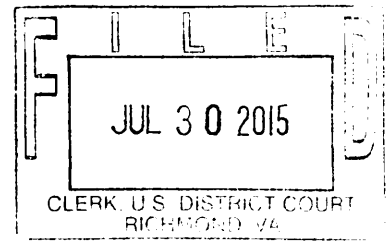


IN THE UNITED STATES DISTRICT COURT  
FOR THE EASTERN DISTRICT OF VIRGINIA  
Richmond Division



SAMSUNG ELECTRONICS CO., LTD,  
et al,

Plaintiffs,

v.

Civil Case No. 3:14-cv-757

NVIDIA CORPORATION,  
et al,

Defendants.

**ORDER**

For the reasons stated on the record on June 29, 2015 and in a forthcoming Memorandum Opinion, the disputed terms are given the following constructions:

- A. "Request ID value/ request ID" is construed to mean a numerical value assigned by the cache controller.
- B. "Shift register for delaying" is construed to mean a circuit including a register for delaying.
- C. "Determin[ed/ing]" is construed according to its plain and ordinary meaning.
- D. "An insulating layer" is construed to mean a structure comprising one or more electrical insulating layers.

- E. "Forming a trench in said substrate, and wherein said field isolation layer fills said trench" is construed to according to its plain and ordinary meaning.
- F. "Insulating spacer along a sidewall" is construed according to its plain and ordinary meaning.
- G. "Reference voltage" is construed to mean a voltage level whose steady value serves as a basis for comparison or operation.
- H. "Controlling propagation delay time" is construed to mean controlling the time from input to output of a signal of a semiconductor memory.
- I. "A gate insulating layer" is construed to mean a gate comprising one or more insulating layers.
- J. "Depositing a third metal gate electrode layer onto the second metal gate electrode layer" is construed to mean depositing a third metal gate electrode layer comprised of one or more metal sublayers onto the second metal gate electrode layer.
- K. "Depositing a second metal gate electrode layer onto inner sidewalls of the spacers and onto an upper surface of the patterned first metal gate electrode layer" is construed to according to its plain and ordinary meaning.
- L. "Sending parallel digital video data" is construed to mean transmitting all bits of a given character of the video data.

M. "Means for generating a cable sensing signal to be sent to said first external video port over the digital cable, thereby informing the video controller of the digital cable connection state of said first external port" has a function which is construed as generating a cable sensing signal to be sent to first external video port over the digital cable. The means are construed to be the DVCC and resistor R1 depicted in Figure 14 of U.S. Patent No. 6,804,724.

N. The preamble of claim 19 in the '938 Patent is construed as not limiting.

It is so ORDERED.

/s/ REP  
Robert E. Payne  
Senior United States District Judge

Richmond, Virginia  
Date: July 30, 2015