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	16/202,067 VLSI Layouts	s of Fully Connected Generalized Networks		V-0070US			
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<ul> <li>+ Fees</li> <li>+ Supplemental Resources &amp;</li> </ul>	Bibliographic Data	Bibliographic Data					
Support	Application Number:	16/202,067	Correspondence Address Customer Number:	38139			
Patent Information	Filing or 371 (c) Date:	11-27-2018	Status:	Docketed New Case - Ready for Examination			
Patent Guidance and General Info + Codes, Rules & Manuals	Application Type:	Re-Issue	Status Date:	02-22-2019			
<ul> <li>Employee &amp; Office Directories</li> <li>Resources &amp; Public Notices</li> </ul>	Examiner Name:	TON, MY TRANG	Location: 🕡	ELECTRONIC			
	Group Art Unit:	3992	Location Date:	-			
Patent Searches	Confirmation Number:	8134	Earliest Publication No:	-			
Patent Official Gazette      Search Patents & Applications	Attorney Docket Number:	V-0070US	Earliest Publication Date:	-			
+ Search Biological Sequences	Class / Subclass:	326/041	Patent Number:	÷			
+ Copies, Products & Services	First Named Inventor:	Venkat Konda , San Jose, CA (US) all Inventors	Issue Date of Patent:	-			
Other First Named Applicant:		Konda Technologies Inc., San Jose, CA (US) all Applicants	International Registration Number (Hague):	-			
Trademarks Policy & Law Reports	Entity Status:	Small	International Registration Publication Date:	-			
	AIA (First Inventor to File):	No					
	Title of Invention: VL	'LSI Layouts of Fully Connected Generalized Networks					
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(PAIR) + Patent Ownership	Select Application New Case Data	Transaction Image File Continuity Address & Assignments Display References		
<ul> <li>Fees</li> <li>Supplemental Resources &amp;</li> </ul>	Transaction History			
Support	Date	Transaction Description		
Patent Information	11-27-2019	Mail-Petition Decision - Granted		
Patent Guidance and General Info	11-27-2019	Petition Decision - Granted		
Codes, Rules & Manuals     Employee & Office Directories	09-23-2019	Preliminary Amendment		
<ul> <li>Employee &amp; Office Directories</li> <li>Resources &amp; Public Notices</li> </ul>	11-27-2018	Preliminary Amendment		
	09-17-2019	Petition Entered		
Patent Searches Patent Official Gazette	09-10-2019	Email Notification		
+ Search Patents & Applications	09-09-2019	Mail Miscellaneous Communication to Applicant		
<ul> <li>+ Search Biological Sequences</li> <li>+ Copies, Products &amp; Services</li> </ul>	09-05-2019	Miscellaneous Communication to Applicant - No Action Count		
	06-13-2019	Email Notification		
Other	06-11-2019	Mail Miscellaneous Communication to Applicant		
Copyrights Trademarks	06-07-2019	Miscellaneous Communication to Applicant - No Action Count		
Policy & Law	12-04-2018	Information Disclosure Statement (IDS) Filed		
Reports	12-04-2018	Information Disclosure Statement (IDS) Filed		
	12-04-2018	Information Disclosure Statement (IDS) Filed		
	12-04-2018	Information Disclosure Statement (IDS) Filed		
	04-18-2019	Email Notification		
	04-17-2019	Mail Miscellaneous Communication to Applicant		
	04-15-2019	Miscellaneous Communication to Applicant - No Action Count		
	04-11-2019	Information Disclosure Statement considered		
	04-11-2019	Information Disclosure Statement considered		
	04-11-2019	Information Disclosure Statement considered		
javascript:submitTab('pair_search')	04-11-2019	Information Disclosure Statement considered		

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12-04-2018	Information Disclosure Statement (IDS) Filed
12-05-2018	Application ready for PDX access by participating foreign offices
12-06-2018	Filing Receipt
12-06-2018	Notice MailedApplication IncompleteFiling Date Assigned
01-01-2019	Notice of Reissue Published in Official Gazette
12-04-2018	Applicant Has Filed a Verified Statement of Small Entity Status in Compliance with 37 CFF
11-27-2018	PTO/SB/69-Authorize EPO Access to Search Results
11-27-2018	Applicants have given acceptable permission for participating foreign
11-28-2018	Cleared by OIPE CSR
11-27-2018	ENTITY STATUS SET TO UNDISCOUNTED (INITIAL DEFAULT SETTING OR STATUS CHANG
11-27-2018	Initial Exam Team nn

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04-11-2019	Information Disclosure Statement considered
04-11-2019	Information Disclosure Statement considered
04-11-2019	Information Disclosure Statement considered
04-11-2019	Information Disclosure Statement considered
03-19-2019	Case Docketed to Examiner in GAU
02-25-2019	Paralegal Reissue Review Complete
02-22-2019	Case Docketed to Examiner in GAU
12-04-2018	Information Disclosure Statement (IDS) Filed
02-21-2019	Email Notification
02-20-2019	Application Dispatched from OIPE
02-20-2019	FITF set to NO - revise initial setting
02-21-2019	Application Is Now Complete
02-21-2019	Filing Receipt - Updated
02-03-2019	Additional Application Filing Fees
12-06-2018	Electronic Review
12-06-2018	Email Notification
12-06-2018	Email Notification
12-04-2018	Information Disclosure Statement (IDS) Filed
12-04-2018	Information Disclosure Statement (IDS) Filed
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	<ul> <li>Global Dossier</li> <li>Order Certified Application As I</li> </ul>				
<ul> <li>Electronic Filing</li> <li>Patent Application Information</li> </ul>	12/601,275 VLSI LAYOUTS OF FULLY CONNECTED GENERALIZED NETWORKS				
(PAIR) + Patent Ownership	Select New Case         Application Transaction Data         Image File History         Patent Term Wrapper         Continuity Adjustments         Fees Data         Published Documents Attorney/Agent         Address & Ass				
<ul> <li>Fees</li> <li>Supplemental Resources &amp;</li> </ul>	Parent Continuity Data				
Support	Description Parent Number Parent Filing or 371(c) Date				
	This application is National Stage Entry ofPCT/US08/6460505-22-2008				
Patent Information	Claims Priority from Provisional Application 60/940,394 05-25-2007				
Patent Guidance and General Info	Child Continuity Data				
<ul> <li>Codes, Rules &amp; Manuals</li> <li>Employee &amp; Office Directories</li> <li>Resources &amp; Public Notices</li> </ul>	16/202,067 filed on 11-27-2018 which is Pending claims the benefit of 12/601,275				
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