

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

INTEL CORPORATION
Petitioner

v.

PACT XPP SCHWEIZ AG
Patent Owner

U.S. Patent No. 9,075,605

Case IPR2020-00541

PETITIONER'S UPDATED EXHIBIT LIST

Pusuant to 37 C.F.R. § 42.63(e), Petitioner Intel Corporation respectfully submits the following updated exhibit list:

PETITIONER’S UPDATED EXHIBIT LIST

Exhibit No.	Description
1001	Declaration of Carl Sechen, Ph.D. (“Ex. 1001”)
1002	Carl Sechen, Ph.D. <i>Curriculum Vitae</i>
1003	U.S. Patent No. 9,075,605 (the “’605 patent”)
1004	Certified File History of U.S. Patent Application No. 13/653,639 (“’605 file history”) (without copies of the prior art references)
1005	U.S. Patent No. 8,099,618 (the “’618 patent”)
1006	U.S. Patent No. 7,444,531 (the “’531 patent”)
1007	U.S. Patent No. 9,552,047 (the “’047 patent”)
1008	Certified File History of U.S. Patent Application No. 14/219,945 (“’047 file history”) (without copies of the prior art references)
1009	U.S. Patent No. 6,141,762 (“Nicol”)
1010	U.S. Patent No. 6,367,023 (“Kling”)
1011	U.S. Patent No. 6,535,798 (“Bhatia”)
1012	U.S. Patent No. 6,052,773 (“DeHon”)
1013	Complaint, <i>XPP Schweiz AG v. Intel Corp.</i> , Case No. 1:19-cv-1006-UNA (D. Del.) (No. 1) (“Complaint”)
1014	Kung, H.T., <i>Why Systolic Architectures?</i> , Computer Vol. 15 (Pub. January 1982)
1015	Reserved
1016	Joint Claim Construction Chart, <i>XPP Schweiz AG v. Intel Corp.</i> , Case No. 1:19-cv-1006-UNA (D. Del.) (No. 67) (“Joint Claim Construction Chart”)
1017	I. Hong, D. Kirovski, Gang Qu, M. Potkonjak and M. B. Srivastava, <i>Power Optimization of Variable-Voltage Core-Based Systems</i> , in 18 IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS, NO. 12, 1702-1714 (Dec. 1999) (“Hong”)
1018	U.S. Patent No. 6,704,877 (“Cline”)

Exhibit No.	Description
1019	RUDOLF F. GRAF, MODERN DICTIONARY OF ELECTRONICS (7th ed. 1999) (“Graf”)
1020	<i>Advanced Configuration and Power Interface</i> (ACPI), Revision 1.0 (December 22, 1996)
1021	Michael Peter Kennedy & Leon O. Chua, <i>Hysteresis in Electronic Circuits: A Circuit Theorist’s Perspective</i> , in 19 INT. JOURNAL OF CIRCUIT THEORY AND APPLICATIONS, 471–515 (1991)
1022	John P. Hayes, <i>Computer Architecture and Organization</i> (3rd ed. 1998)
1023	Reserved
1024	Reserved
1025	Declaration of Rachel J. Watters in Support of Public Availability of <i>Hysteresis in Electronic Circuits: A Circuit Theorist’s Perspective</i> , in 19 INT. JOURNAL OF CIRCUIT THEORY AND APPLICATIONS, 471–515 (1991) (Kennedy)
1026	U.S. Patent No. 6,397,343 (“Williams”)
1027-1032	Intentionally Left Blank
1033	Power of Attorney, U.S. Patent No. 8,471,593, filed in 2014
1034	Power of Attorney, U.S. Patent No. 7,928,763, filed in 2014
1035	Power of Attorney, U.S. Patent No. 9,552,047, filed in 2014
1036	Power of Attorney, U.S. Patent No. 9,037,807, filed in 2014
1037	Patent Assignment Agreement between PACT XPP Technologies AG & Scientia Sol Mentis AG, executed March 15, 2018
1038	Email chain between K. Bendix and A. Grunberger re acceptance of service, dated February 5-6, 2020
1039	Email chain between L. Tarpley and A. Grunberger re acknowledging receipt of electronic service, dated February 7 and 10, 2020
1040	Email chain between A. Grunberger and K. Bendix re service, dated February 10, 2020
1041	Power of Attorney, U.S. Patent No. 7,928,763 filed in 2020

Exhibit No.	Description
1042	Affidavit of Raymundo Avila with Nationwide Legal re hand-service of Petitioner Intel Corporation's Petitions and Supporting Documents on PACT counsel on February 10, 2020 and Delivery Receipt
1043	Intel Corporation's Complaint for Declaratory Judgment, Dkt. No. 1, filed April 25, 2019
1044	Letter from S. Li to S. Lotfollahi re PACT's claim swapping and narrowing, dated December 13, 2019
1045	Intentionally Left Blank
1046	Screenshot from Public PAIR Correspondence Record, U.S. Patent No. 9,552,047, taken February 10, 2020
1047	Request for Certificate of Correction, U.S. Patent No. 9,075,605, filed December 5, 2018

Date: July 23, 2020

Respectfully Submitted,

/s/ Kevin Bendix

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CERTIFICATE OF SERVICE

The undersigned hereby certifies that a copy of the foregoing documents were served on July 23, 2020 through the Patent Review Processing System, as well as by e-mailing copies to:

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