

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SAMSUNG ELECTRONICS CO., LTD.,
Petitioner

v.

ACORN SEMI, LLC,
Patent Owner

Case No. IPR2020-01264
Patent No. 8,766,336

PATENT OWNER'S EXHIBIT LIST

Pursuant to 37 C.F.R. § 42.63(e), Acorn provides the following exhibit list:

Exhibit No.	Document
2001	Statutory Disclaimer filed in U.S. Patent No. 8,766,336 (Oct. 14, 2020).
2002-2004	<i>Reserved</i>
2005	Prosecution History Excerpts of U.S. Patent Appl. No. 15/929,592 as of Oct. 12, 2020.
2006	Prosecution History Excerpts of U.S. Patent Appl. No. 15/929,593 as of Oct. 12, 2020.
2007	<i>Reserved</i>
2008	3d Am. Docket Control Order, <i>Acorn Semi, LLC v. Samsung Elecs. Co.</i> , No. 2:19-cv-003347-JRG, Dkt. # 57 (E.D. Tex. July 21, 2020).
2009	<i>Reserved</i>
2010	Order Denying Mot. for Stay, <i>Acorn Semi, LLC v. Samsung Elecs. Co.</i> , No. 2:19-cv-003347-JRG, Dkt. # 72 (E.D. Tex. Sept. 14, 2020).
2011	<i>Reserved</i>
2012	<i>Image Processing Technologies LLC v. Samsung Electronics. Co.</i> , No. 2:16-cv-505-JRG, 2017 WL 7051628 (E.D. Tex. Feb. 17, 2017).
2013	<i>Intellectual Ventures I LLC v. T Mobile USA, Inc.</i> , No. 2:17-CV-00577-JRG (E.D. Tex. Dec. 13, 2018).
2014	<i>Oyster Optics, LLC v. Infinera Corp.</i> , No. 2:19-CV-00257-JRG (E.D. Tex. July 17, 2020).

Exhibit No.	Document
2015	<i>Solas OLED Ltd. v. Samsung Display Co.</i> , No. 2:19-CV-00152-JRG (E.D. Tex. July 17, 2020).
2016	Standing Order re. . . . COVID-19 Pandemic, avail. at http://www.txed.uscourts.gov/sites/default/files/judgeFiles/COVID19%20Standing%20Order.pdf , (E.D. Tex. Apr. 20, 2020).
2017	Def.’s Prelim. Invalidity Contentions – Cover Pleading, <i>Acorn Semi, LLC v. Samsung Elecs. Co.</i> , No. 2:19-cv-003347-JRG (E.D. Tex. served May 19, 2020).
2018-2019	<i>Reserved</i>
2020	App’x B6, Def.’s Prelim. Invalidity Contentions, <i>Acorn Semi, LLC v. Samsung Elecs. Co.</i> , No. 2:19-cv-003347-JRG (E.D. Tex. served May 19, 2020).
2021	App’x B10, Def.’s Prelim. Invalidity Contentions, <i>Acorn Semi, LLC v. Samsung Elecs. Co.</i> , No. 2:19-cv-003347-JRG (E.D. Tex. served May 19, 2020).
2022-2041	<i>Reserved</i>
2042	R. Butz, et al., “Chemical Bonding & Reactions at Ti/Si and Ti/Oxygen/Si Interfaces,” J. Vac. Sci. Tech. A1(2), pp. 771-75 (Apr.-June 1983).
2043	Declaration of Dr. Stephen M. Goodnick (“Goodnick Decl.”).
2044	<i>Curriculum Vitae</i> for Dr. Stephen M. Goodnick.
2045	D. Connelly, et al., “A New Route to Zero-Barrier Metal Source/Drain MOSFETs,” IEEE Trans. on Nanotech., Vol. 3, No. 1, pp. 98-104 (Mar. 2004).

Exhibit No.	Document
2046	D. Connelly, et al., “Fermi-Level Depinning for Low-Barrier Schottky Source/Drain Transistors,” Appl. Physics Ltrs. 88, 012105 (2006).
2047	Min-Hyun Lee, et al., “2D Inserted Metal/Semiconductor Interface: An Attractive Candidate for Semiconductor MOSFET Contact,” available at http://phantomsfoundation.com/RPGR/2017/Abstracts/RPGR2017_Lee_Min-Hyun_123.pdf (2017).
2048	Min-Hyun Lee, et al., “Two-Dimensional Materials Inserted at the Metal/Semiconductor Interface: An Attractive Candidate for Semiconductor Device Contact,” Nano Letters (2018).
2049	P. Paramahans, et al. “Fermi-level unpinning and low resistivity in contacts to n-type Ge with a thin ZnO interfacial layer,” Appl. Physics Ltrs. 101, 182105 (2012).
2050	J. Hu, et al. “Metal/III-V effective barrier height tuning using atomic layer deposition of high-j/high-j bilayer interfaces,” Appl. Physics Ltrs. 99, 092107 (2011).
2051	A. Agrawal, et al. “A unified model for insulator selection to form ultra-low resistivity metal-insulator-semiconductor contacts to n-Si, n-Ge, and n-InGaAs,” Appl. Physics Ltrs. 101, 042108 (2012)
2052	CRC Handbook of Chem. & Physics, pp. 12-46 – 12-48 (76th Ed. 1995-96)

Respectfully submitted,

Date: 2020 Oct. 16

By: / M.C. Phillips /
Matthew C. Phillips
Registration No. 43,403
Lead Counsel for Acorn

CERTIFICATE OF SERVICE

I hereby certify that on October 16, 2020, copies of the foregoing PATENT OWNER'S EXHIBIT LIST and all documents filed with it were served via electronic mail, as agreed to by counsel, upon the following counsel for the patent owner:

John M. Desmarais: jdesmarais@desmaraisllp.com

Theodoros Konstantakopoulos: tkonstantakopoulos@desmaraisllp.com

Cosmin Maier: cmaier@desmaraisllp.com

Yung-Hoon Ha: yha@desmaraisllp.com

/ M.C. Phillips /
Matthew C. Phillips
Registration No. 43,403