

Filed: March 22, 2021

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

MICROCHIP TECHNOLOGY INCORPORATED; AND
MICROSEMI CORPORATION,

Petitioners,

v.

BELL SEMICONDUCTOR, LLC,

Patent Owner.

Case No. IPR2020-00191

U.S. Patent No. 7,221,173

PATENT OWNER'S EXHIBIT LIST

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Patent Owner Bell Semiconductor, LLC hereby submits its exhibit list to accompany the submission of Exhibits 2001 – 2008:

EXHIBIT	DESCRIPTION
2001.	Agere Systems Invention Submission form dated May 10, 2004.
2002.	<i>Bell Semiconductor, LLC v. Microchip Tech.</i> , No. 6:20-cv-00296-ADA, Dkt. 37 (Scheduling Order) (W.D. Tex. Nov. 20, 2020).
2003.	<i>Bell Semiconductor, LLC v. Microchip Tech.</i> , No. 6:20-cv-00296-ADA, Defendants' Preliminary Invalidity Contentions (W.D. Tex. Dec. 4, 2020).
2004.	Declaration of Mr. Kouros Azimi.
2005.	Declaration of Dr. Sailesh Merchant.
2006.	LOIS YONG, TU ANH TRAN, STEPHEN LEE, BILL WILLIAMS and JODY ROSS, "Novel Method of Separating Probe and Wire Bond Regions Without Increasing Die Size," 2003 Electronic Components and Technology Conference, pp. 1323-1329.
2007.	Copy of U.S. Patent Application No. 2013/0299947 A1
2008.	The Parametric Handbook, Excerpt Ed. (March 2012) available at https://www.keysight.com/upload/cmc_upload/All/1-Parametric_Test_Basic.pdf (last visited March 17, 2021).

Dated: March 22, 2021

Respectfully Submitted,

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CERTIFICATE OF SERVICE

The undersigned hereby certifies that a copy of the foregoing Patent Owner's Exhibit List was served on March 22, 2021, by delivering a copy via electronic mail to the attorneys of record for the Petitioner as follows:

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