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Filed on behalf of STMicroelectronics, Inc.

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UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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STMICROELECTRONICS, INC.  
Petitioner

v.

THE TRUSTEES OF PURDUE UNIVERSITY  
Patent Owner

IPR2022-00252  
U.S. Patent No. 7,498,633

**PETITIONER'S UPDATED EXHIBIT LIST**

**TABLE OF EXHIBITS**

<b>Exhibit</b>	<b>Description</b>
1001	U.S. Patent No. 7,498,633
1002	Declaration of Dr. Vivek Subramanian
1003	U.S. Patent Application Publication No. 2004/0119076 (“ <i>Ryu</i> ”)
1004	U.S. Patent No. 6,413,822 (“ <i>Williams</i> ”)
1005	U.S. Patent No. 5,233,215 (“ <i>Baliga</i> ”)
1006	U.S. Patent No. 6,316,791 (“ <i>Schörner</i> ”)
1007	U.S. Patent Application No. 2006/0267092 (“ <i>Jun</i> ”)
1008	’633 Patent File History, 4/4/2007 Non-Final Rejection
1009	’633 Patent File History, 8/6/2007 Response to Office Action
1010	’633 Patent File History, 10/12/2007 Final Rejection
1011	’633 Patent File History, 3/12/2008 Response to Office Action
1012	’633 Patent File History, 6/25/2008 Non-Final Rejection
1013	’633 Patent File History, 8/22/2008 Response to Office Action
1014	’633 Patent File History, 12/17/2008 Notice of Allowance
1015	U.S. Patent No. 5,317,184 (“ <i>Rexer</i> ”)
1016	“Modern Power Devices,” B. Jayant Baliga, 1987 (relevant sections)

Exhibit	Description
1017	A. G. Jost et al., “Implementation of a VLSI Layout Tool on Personal Computers,” ACM, 1990
1018	E. S. Kuh et al., “Recent Advances in VLSI Layout,” Proceedings of the IEEE, vol 78, no. 2, February 1990
1019	“Power MOSFETs – Theory and Applications,” Duncan A. Grant and John Goward, 1989 (“ <i>Grant</i> ”) (relevant sections)
1020	J. A. Cooper, Jr. et al., “SiC Power-Switching Devices—The Second Electronics Revolution?,” Proceedings of the IEEE, vol. 90, no. 6, June 2002
1021	U.S. Patent No. 5,510,281 (“ <i>Ghezze</i> ”)
1022	J. A. Cooper, Jr. et al., “Status and Prospects for SiC Power MOSFETs,” IEEE Transactions on Electron Devices, vol. 49, no. 4, April 2002
1023	S-H Ryu et al., “10 A, 2.4 kV Power DiMOSFETs in 4H-SiC,” IEEE Electron Device Letters, vol. 23, no. 6, June 2002
1024	S-H Ryu et al., “2 kV 4H-SiC DMOSFETs for Low Loss, High Frequency Switching Applications,” International Journal of High Speed Electronics and Systems, vol. 14, no. 3, 2004
1025	Plaintiff’s Disclosure of Earliest Date of Invention
1026	Scheduling Order, <i>The Trustees of Purdue University v. STMicroelectronics, Inc. et al.</i> , No. 6:21-cv-00727 (W.D. Tex.), Dkt. 45 (November 22, 2021)
1027	Statistics from Docket Navigator showing active patent cases before Judge Alan Albright of the U.S. District Court for the Western District of Texas (as of December 6, 2021)
1028	Article entitled “District Court Trial Dates Tend to Slip After PTAB Discretionary Denials” (July 24, 2020)

Exhibit	Description
1029	Modified Claim Construction Order, <i>The Trustees of Purdue Univ. v. STMicroelectronics N.V., STMicroelectronics, Inc., STMicroelectronics Int'l</i> , Case No. 6:21-cv-00727, Dkt. 90 (W.D. Tex. May 9, 2022)

Respectfully submitted,

Dated: May 24, 2022

/Scott Bertulli/  
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**CERTIFICATE OF SERVICE**

I hereby certify that on May 24, 2022, I caused a true and correct copy of the foregoing Petitioner's Updated Exhibit List and Exhibit 1029 to be served electronically to the following lead and backup counsel for Patent Owner to the following email addresses:

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