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Asmita Saha ?

Also published under: A. Saha

Affiliation

Purdue Univ., Lafayette

Publication Topics

power MOSFET, silicon compounds, semiconductor doping, semiconductor process modelling, wide band gap semiconductors

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Biography

Asmita Saha received the B.E. degree in electronics and telecommunication engineering from Jadavpur University, Kolkata, India, in 1997, the M.Tech. degree in electronics and electrical communication engineering from the Indian Institute of Technology, Kharagpur, India, in 1999, and the Ph.D. degree from the School of Electrical and Computer Engineering, Birk Nanotechnology Center, Purdue University, West Lafayette, IN, in 2006. Her thesis focused on optimized design, and simulation and fabrication of 4H-SiC short-channel DMOSFETs.,From 1999 to 2001, she was a Lecturer with the Department of Electronics, Jadavpur University. She is currently with Intel Corporation as a Process Engineer. (Based on document published on 24 September 2007).

Publications

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Co-Authors:

J. A. Cooper

M. Matin

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A 1-kV 4H-SiC Power DMOSFET Optimized for Low on-Resistance

Asmita Saha; James A. Cooper

IEEE Transactions on Electron Devices

Year: 2007 | Volume: 54, Issue: 10 | Journal Article | Publisher: IEEE

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A self-aligned process for high-voltage, short-channel vertical DMOSFETs in 4H-SiC

M. Matin; A. Saha; J.A. Cooper

IEEE Transactions on Electron Devices

Year: 2004 | Volume: 51, Issue: 10 | Journal Article | Publisher: IEEE

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