p-GaN/AlGaN/GaN Enhancement-Mode HEMTs

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GaN-based enhancement-mode (E-mode) HEMTs are attracting significant interest for integration of control circuitry and for the added safety of a normally-off device in power switching applications. While previous work reports excellent performance by gate-recessing¹ and Fluorine-based plasma treatment², the Schottky gate turn-on voltage of these devices are at most 2 V. Because high-power switching applications require a threshold voltage of over 1 V for gate signal noise immunity, increasing the gate turn-on voltage is crucial. Utilization of p-GaN barrier below the gate³ depletes the channel and increases the gate turn-on voltage to 3 V, rendering it attractive for high-power applications. In this report we present a p-GaN/AlGaN/GaN E-mode HEMTs with a 3 V gate turn-on and maximum output current exceeding 0.3 A/mm. In addition, pulsed I-V measurement and small-signal performance of these devices are presented and the design space of p-GaN gated E-mode HEMTs are investigated for high-power switching applications.

The devices used for this study were fabricated on material grown by RF plasma-assisted molecular beam epitaxy. The schematic cross section of the device is shown in Fig. 1. The Mg doping concentration in the 10 nm-thick p-GaN layer was approximately 1×10^{18} cm⁻³ and the thickness of the Al_{0.22}Ga_{0.78}N layer was 12 nm. The p-GaN in the source/drain regions was etched away prior to ohmic metal deposition using a self-aligned dry etch while the p-GaN in the access regions was etched away using the ohmic contacts and the gate electrode as masks. Peak g_m of 160 mS/mm is reached at $V_{\rm GS}$ of 1.4 V and the threshold voltage is 0.5 V as shown in Fig. 2. The 3 V turn-on of the gate diode is approximately 1 V higher than standard Schottky gates as shown in Fig. 3.

The DC, 80 μ s, and 200 ns-pulsed I-V output characteristics of the device are shown in Fig. 4. The maximum output current of the device is the highest reported among p-GaN gated E-mode HEMTs. The kink in the pulsed I-V, which may be caused by traps in the p-GaN/AlGaN interface, is currently under investigation. Consequently, the device exhibits f_T of 12 GHz and f_{max} of 35 GHz which are lower than that of depletion-mode (D-mode) devices with similar dimensions. Similar behavior has been observed and solved in other GaN-based HEMTs that have GaN/AlGaN interfaces with net a negative polarization charge⁴. Application of similar solutions is currently being evaluated as well.

In order to realize a p-GaN gated device with a threshold voltage of 1 V and high current density, calculations indicate the need for an AlN interlayer due to trade-off between threshold voltage and access resistance as shown in Fig. 5, and 6. Without an AlN interlayer, threshold voltage of 1 V can be achieved, but the device will exhibit extremely low current due to high access resistance. With an AlN interlayer, threshold voltage of 1 V can be achieved with much lower access resistance. Work is in progress to develop p-GaN/AlGaN/AlN/GaN E-mode HEMTs with a threshold voltage of 1 V and high current density.

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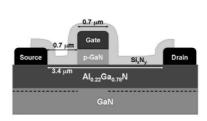


Fig. 1: Schematic cross section of the p-GaN/AlGaN/GaN E-mode HEMT.

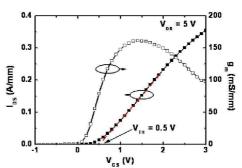


Fig. 2: The gate transfer characteristics. Peak $g_m\!=\!160$ mS/mm at $V_{GS}\!=\!1.4~V$ and V_T is 0.5 V.

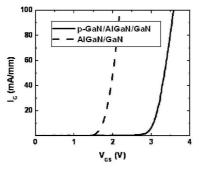


Fig. 3: I-V characteristics of gate diodes.

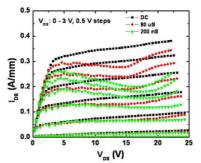
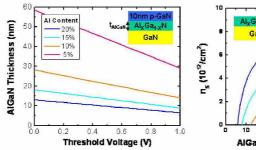


Fig. 4: The DC, $80\mu s$, and 200 ns pulsed current-voltage output characteristics.



Algan Barrier Thickness (rmm)

Fig. 5: AlGaN thickness vs. threshold voltage, sheet charge vs. AlGaN thickness for p-GaN/AlGaN/GaN structure (calculation). Sheet charge density is very low at AlGaN thickness required for threshold voltage of 1 V.

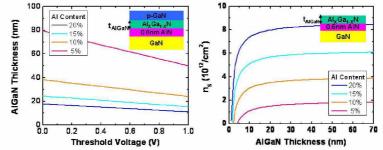


Fig. 6: AlGaN thickness vs. threshold voltage, sheet charge vs. AlGaN thickness for p-GaN/AlGaN/AlN/GaN structure (calculation). Sheet charge density is high at AlGaN thickness required for threshold voltage of 1 V.